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Substitute for form 1449/PTO

# INFORMATION DISCLOSURE STATEMENT BY APPLICANT

**(Use as many sheets as necessary)**

Sheet	1	of	2
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**Complete if Known**

Application Number	N/A
Filing Date	Herewith
First Named Inventor	Joseph A. Reynick
Art Unit	MTA 2829
Examiner Name	<del>N/A</del> Robert
Attorney Docket Number	REYNICK 2

## U. S. PATENT DOCUMENTS

[illegible]

## FOREIGN PATENT DOCUMENTS

[illegible]

**Examiner  
Signature**

Date  
Considered

7-72-04

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. <sup>1</sup> Applicant's unique citation designation number (optional). <sup>2</sup> See Kinds Codes of USPTO Patent Documents at [www.uspto.gov](http://www.uspto.gov) or MPEP 901.04. <sup>3</sup> Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). <sup>4</sup> For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. <sup>5</sup> Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. <sup>6</sup> Applicant is to place a check mark here if English language Translation is attached.


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Sheet	2	of	2	Attorney Docket Number	REYNICK 2

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
		<del>Parent application Serial No. 09/558,130 filed on April 25, 2000 to Joseph A. Reynick, entitled "INTEGRATED CIRCUIT EARLY LIFE FAILURE DETECTION BY MONITORING CHANGES IN CURRENT SIGNATURES", allowed November 12, 2003.</del>	
RK		Thibeault, "A NOVEL PROBABILISTIC APPROACH FOR IC DIAGNOSIS BASED ON DIFFERENTIAL QUIESCENT CURRENT SIGNATURES", 15th IEEE VLSI Test Symposium, 27 April-1 May 1997, pp. 80-85.	—
RK		Thibeault et al. "DIAGNOSIS METHOD BASED ON DELTA-IDDQ PROBABILISTIC SIGNATURES: EXPERIMENTAL RESULTS"; IEEE International Test Conference, 1998 (Month Unavailable), pp. 1019-1026.	—
RK		Thibeault, "ON THE COMPARISON OF DELTA-IDDQ AND IDDQ TESTING," VLSI Test Symposium, 17th IEEE Proceedings, 25-29 April 1999, pp. 143-150.	—
RK		Thibeault, "IMPROVING DELTA-IDDQ-BASED TEST METHODS", IEEE International Test Conference Proceedings, 3-5 Oct. 2000; pp. 207-206.	—

Examiner Signature		Date Considered	7-22-04
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